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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Jack Hwang et al.

Serial No.: 10/761,550

Filed: January 21, 2004

For: Creating Shallow
Junction Transistors

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Art Unit: 2818

Examiner: Ngan V. Ngo

Atty Docket: ITL.0945US
P15969

Assignee: Intel Corporation

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the restriction requirement mailed February 18, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: February 25, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden
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